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SERIAL NUMBER 10/805,933	FILING DATE 03/22/2004  RULE	CLASS 257	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. JCLA10375-D
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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

*YES, CAL*  
 This application is a DIV of 10/447,520 05/28/2003, NOW US PATENT NO. 6,869,809

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

*YES, CAL*  
 TAIWAN 92103361 02/19/2003

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/31/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 5	TOTAL CLAIMS 4	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>[Signature]</i>	INITIALS <i>CAL</i>		
Verified and Acknowledged				

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## TITLE

Wafer level testing and bumping process

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